

# 650V 45mohm Silicon Carbide Power MOSFET

## AKC65M045WDM

### Features:

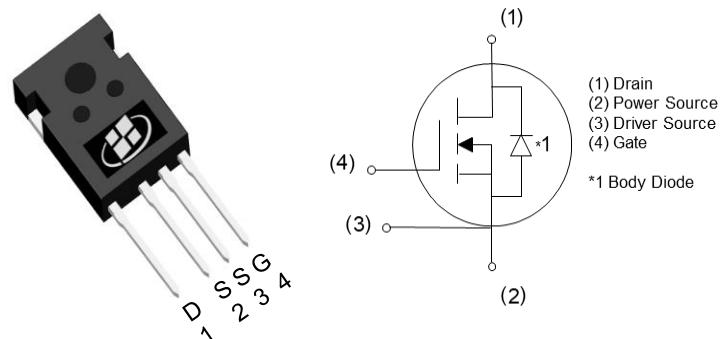
- High-speed switching performance
- Best thermal conductivity and behavior
- Fast intrinsic diode with low reverse recovery ( $Q_{RR}$ )
- Halogen-free, RoHS compliant <sup>(Note 1)</sup>

### Applications:

- Motor drives
- Switched mode power supplies

### Key Performance Parameters:

Parameter	Value	Unit
$V_{DS}$	650	V
$R_{DS(on)}$ , TYP @ $V_{GS} = 15$ V	41	mΩ
$R_{DS(on)}$ , TYP @ $V_{GS} = 18$ V	34	mΩ
$I_D$	50	A
$P_D$	214	W



### Ordering Information:

Ordering Code	Package Type	Marking Code	Form	Packing
AKC65M045WDM	TO-247LP-4L	C65M045WDM	Tube	See the detail package information

### Notes:

1. Contact ALKAIDSEMI sales for detail information

## Maximum Ratings ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Value	Units
$V_{DS}$	Drain-Source Voltage	650	V
$I_D$	Drain Current - Continuous ( $T_C = 25^\circ\text{C}$ ) <sup>(Note 1)</sup>	50	A
	Drain Current - Continuous ( $T_C = 100^\circ\text{C}$ ) <sup>(Note 1)</sup>	35	A
$I_{DM}$	Drain Current - Pulsed <sup>(Note 2)</sup>	135	A
$V_{GS}$	Gate-Source Voltage (dynamic)	-10/+22	V
$V_{GS}$	Gate-Source Voltage (static)	-6/+18	V
$P_D$	Power Dissipation ( $T_C = 25^\circ\text{C}$ )	214	W
$T_J, T_{STG}$	Operating and Storage Temperature Range	-55 to +175	°C

## Thermal Characteristics

Symbol	Parameter	Value	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Steady-State	0.7	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Steady-State	40	°C/W

### Notes:

1. The max drain current limited by maximum junction temperature
2. Repetitive Rating: Pulse width limited by maximum junction temperature

## Electrical Characteristics ( $T_J = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
<b>Static Characteristics</b>						
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}} = 0 \text{ V}, I_D = 100 \mu\text{A}$	650			V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{\text{DS}} = 650 \text{ V}, V_{\text{GS}} = 0 \text{ V}$		5	50	$\mu\text{A}$
$I_{\text{GSS}}$	Gate Leakage Current	$V_{\text{GS}} = +22 \text{ V}, V_{\text{DS}} = 0 \text{ V}$			100	nA
		$V_{\text{GS}} = -10 \text{ V}, V_{\text{DS}} = 0 \text{ V}$			100	nA
$V_{\text{GS(TH)}}$	Gate Threshold voltage	$V_{\text{DS}} = V_{\text{GS}}, I_D = 5 \text{ mA}$	1.8	2.8	4	V
		$V_{\text{DS}} = V_{\text{GS}}, I_D = 5 \text{ mA}, T_J = 175^\circ\text{C}$		2.1		V
$R_{\text{DS(ON)}}$	Drain-Source on-state resistance	$V_{\text{GS}} = 15 \text{ V}, I_D = 20 \text{ A}$		41	60	$\text{m}\Omega$
		$V_{\text{GS}} = 15 \text{ V}, I_D = 20 \text{ A}, T_J = 175^\circ\text{C}$		60		$\text{m}\Omega$
$G_{\text{FS}}$	Forward Transconductance	$V_{\text{DS}} = 20 \text{ V}, I_D = 20 \text{ A}$		12		S
		$V_{\text{DS}} = 20 \text{ V}, I_D = 20 \text{ A}, T_J = 175^\circ\text{C}$		11		S

## Dynamic Characteristics

$C_{\text{ISS}}$	Input Capacitance	$V_{\text{DS}} = 600 \text{ V}, V_{\text{GS}} = 0 \text{ V}, F = 100 \text{ kHz}, V_{\text{AC}} = 25 \text{ mV}$		2600		pF
$C_{\text{OSS}}$	Output Capacitance			145		pF
$C_{\text{RSS}}$	Reverse Transfer Capacitance			5		pF
$E_{\text{OSS}}$	$C_{\text{OSS}}$ Stored Energy			32		$\mu\text{J}$
$R_G$	Gate Resistance	$F = 1 \text{ MHz}, V_{\text{AC}} = 25 \text{ mV}$		1.4		$\Omega$
$Q_{\text{GS}}$	Gate-Source Charge	$V_{\text{DS}} = 400 \text{ V}, I_D = 20 \text{ A}, V_{\text{GS}} = -4/+15 \text{ V}$		28.5		nC
$Q_{\text{GD}}$	Gate-Drain Charge			18		nC
$Q_G$	Total Gate Charge			81		nC

## Switching Characteristics (Note3)

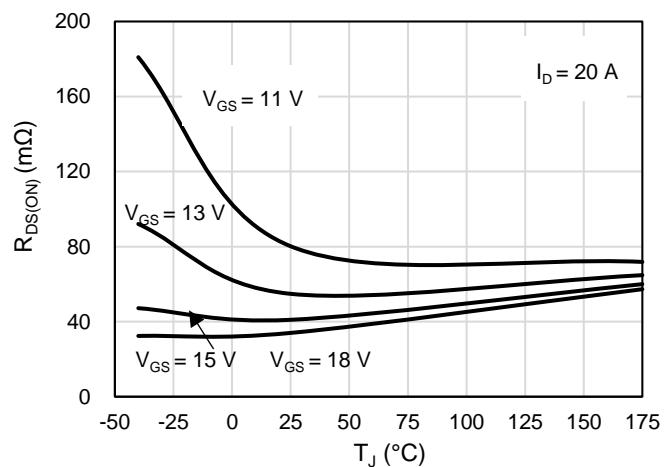
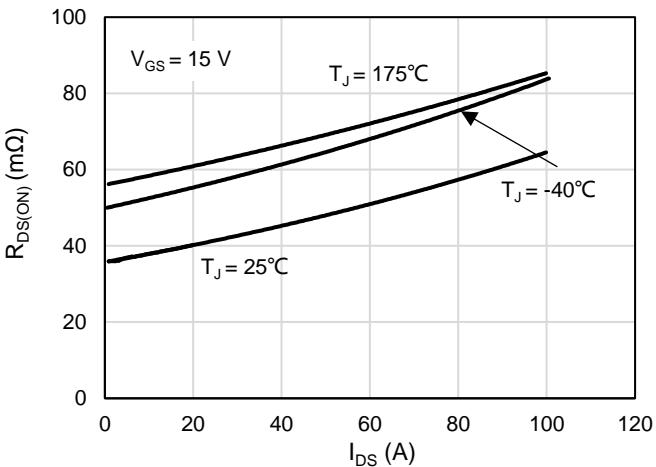
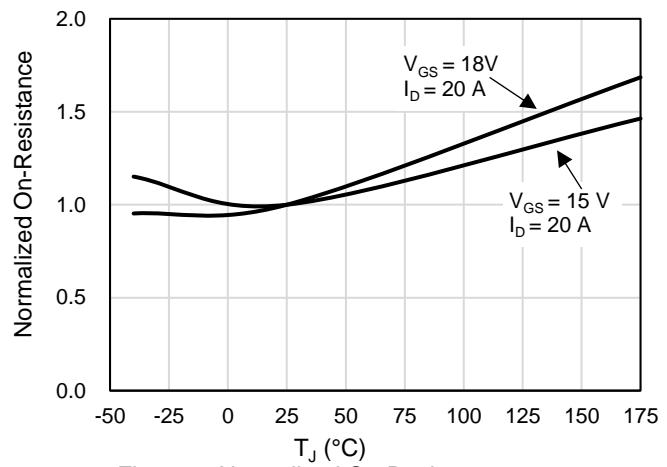
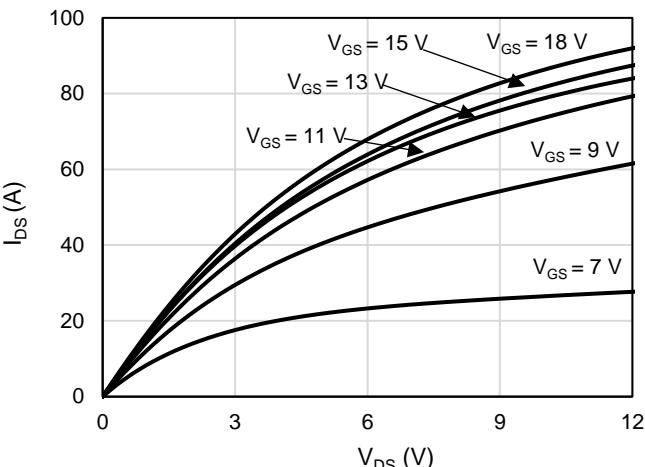
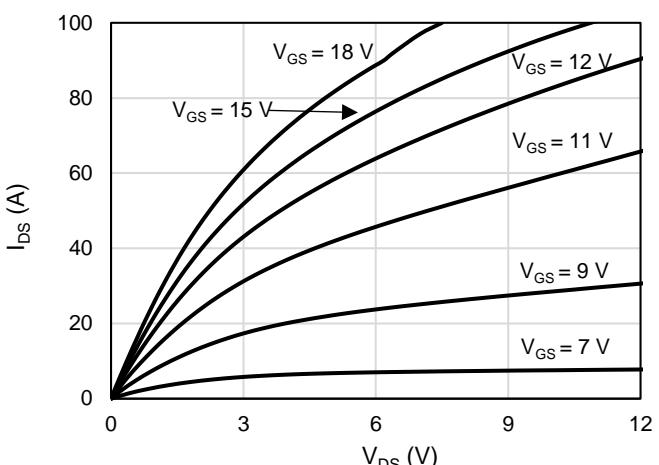
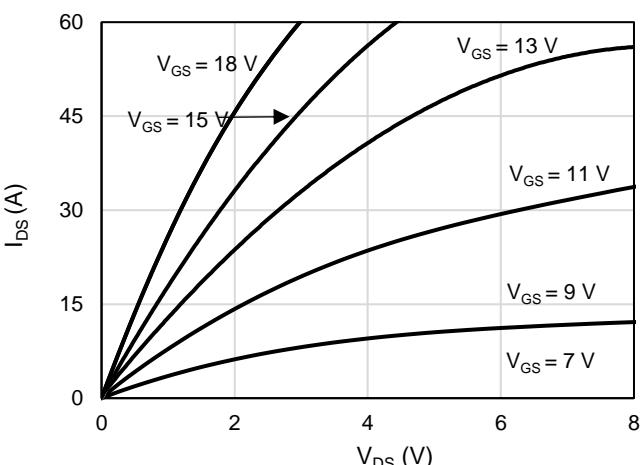
Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
$T_{D(ON)}$	Turn On Delay Time	$V_{DD} = 400 \text{ V}$ , $I_D = 20 \text{ A}$ , $V_{GS} = -4/+15 \text{ V}$ , $R_{G,EXT} = 2.5 \Omega$ $L = 99 \mu\text{H}$ Diode: Body Diode at $V_{GS} = -4\text{V}$		11		ns
$T_R$	Rise Time			10.6		ns
$T_{D(OFF)}$	Turn Off Delay Time			29.5		ns
$T_F$	Fall Time			14.8		ns
$E_{ON}$	Turn On Energy			44.7		$\mu\text{J}$
$E_{OFF}$	Turn Off Energy			21.4		$\mu\text{J}$
$T_{D(ON)}$	Turn On Delay Time			9.5		ns
$T_R$	Rise Time			9.2		ns
$T_{D(OFF)}$	Turn Off Delay Time			35.5		ns
$T_F$	Fall Time			23.4		ns
$E_{ON}$	Turn On Energy	$V_{GS} = -4\text{V}$ , $T_J = 175^\circ\text{C}$		30.8		$\mu\text{J}$
$E_{OFF}$	Turn Off Energy			20		$\mu\text{J}$

Note3: All switching characteristics reference TO247-4L.

## Drain-Source Diode Characteristics ( $T_J = 25^\circ\text{C}$ unless otherwise noted)

$I_S$	Maximum Continuous Drain-Source Diode Forward Current			50	A
$I_{SM}$	Maximum Pulsed Drain-Source Diode Forward Current			135	A
$V_{SD}$	Diode Forward Voltage	$V_{GS} = -4 \text{ V}$ , $I_{SD} = 10 \text{ A}$		4.8	V
		$V_{GS} = -4 \text{ V}$ , $I_{SD} = 10 \text{ A}$ , $T_J = 175^\circ\text{C}$		4.2	V
$I_{RM}$	Peak Reverse Recovery Current	$V_{GS} = -4 \text{ V}$ , $I_{SD} = 20 \text{ A}$ , $V_R = 400 \text{ V}$ , $di/dt = 5000 \text{ A}/\mu\text{s}$		17	A
$T_{RR}$	Reverse Recovery Time			15	ns
$Q_{RR}$	Reverse Recovery Charge			154	nC
$I_{RM}$	Peak Reverse Recovery Current	$V_{GS} = -4 \text{ V}$ , $I_{SD} = 20 \text{ A}$ , $V_R = 400 \text{ V}$ , $di/dt = 1500 \text{ A}/\mu\text{s}$ $T_J = 175^\circ\text{C}$		22	A
$T_{RR}$	Reverse Recovery Time			17	ns
$Q_{RR}$	Reverse Recovery Charge			219	nC

## Electrical Characteristics Diagrams



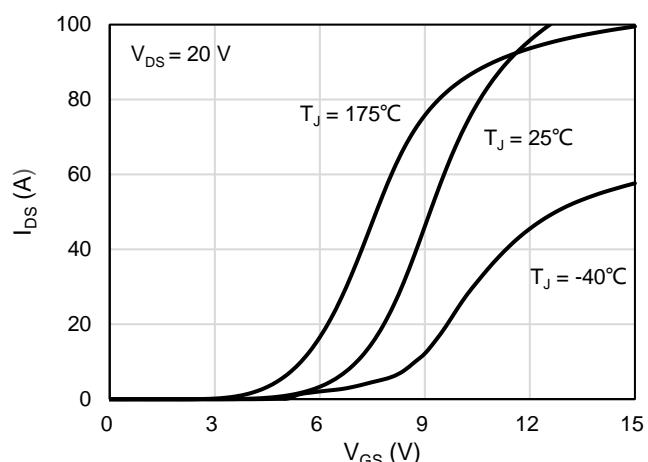


Figure 7: Transfer Characteristics For Various Junction Temperature

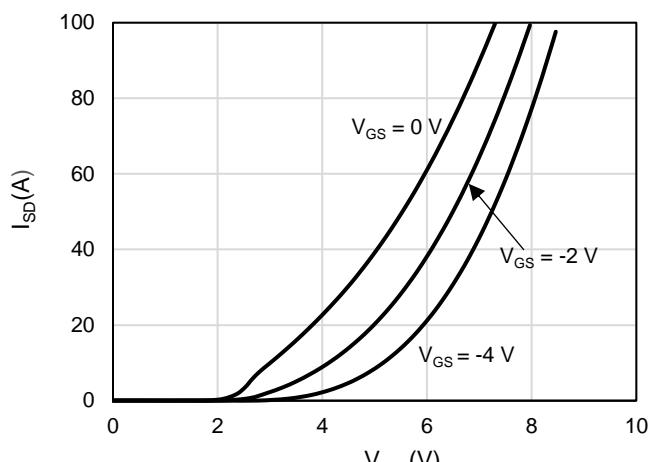
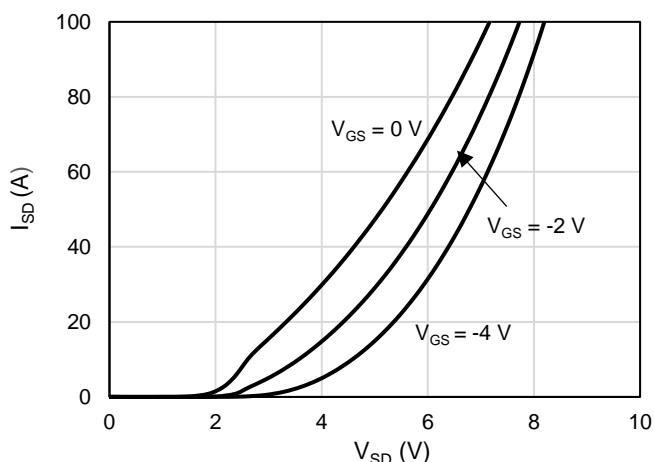
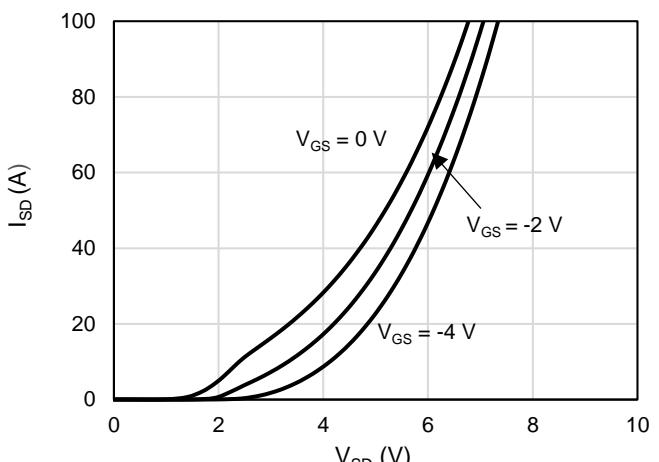
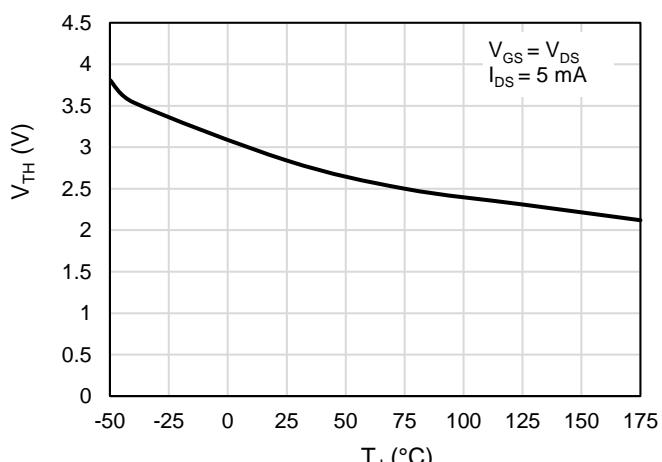
Figure 8: Body Diode Characteristics at  $-40^\circ\text{C}$ Figure 9: Body Diode Characteristics at  $25^\circ\text{C}$ Figure 10: Body Diode Characteristics at  $175^\circ\text{C}$ 

Figure 11: Threshold Voltage vs. Temperature

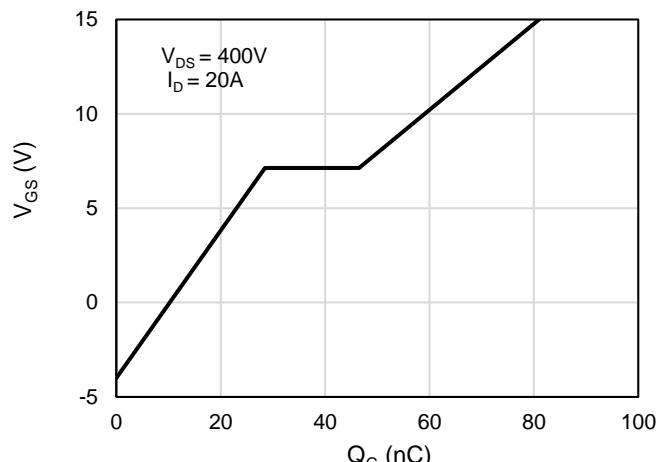
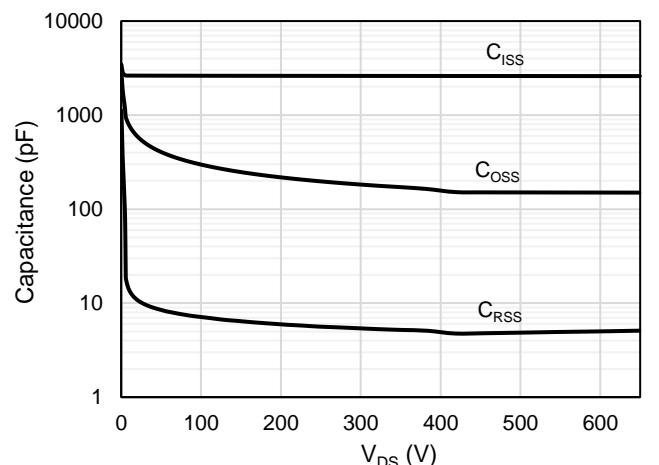
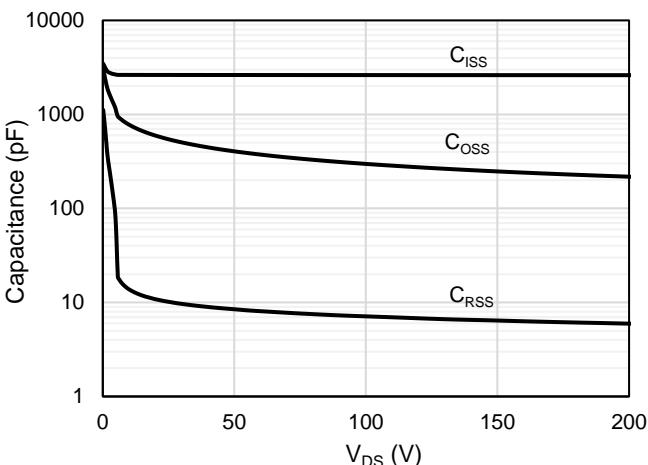
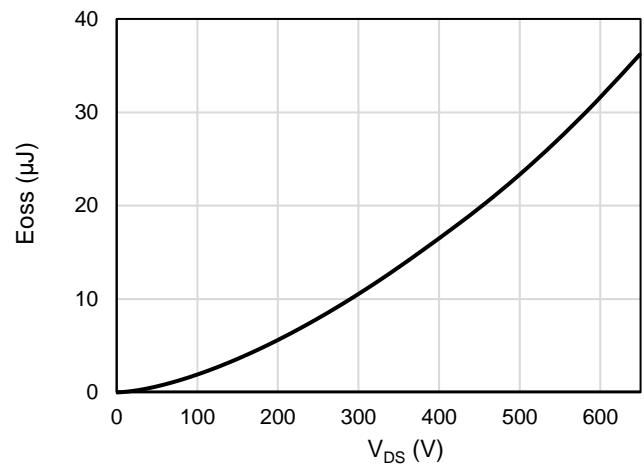
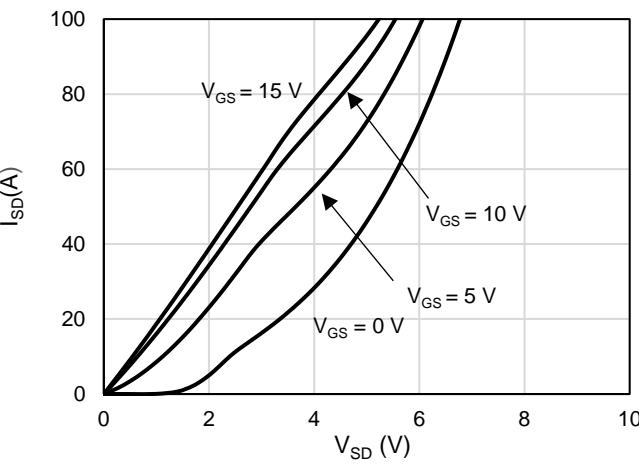
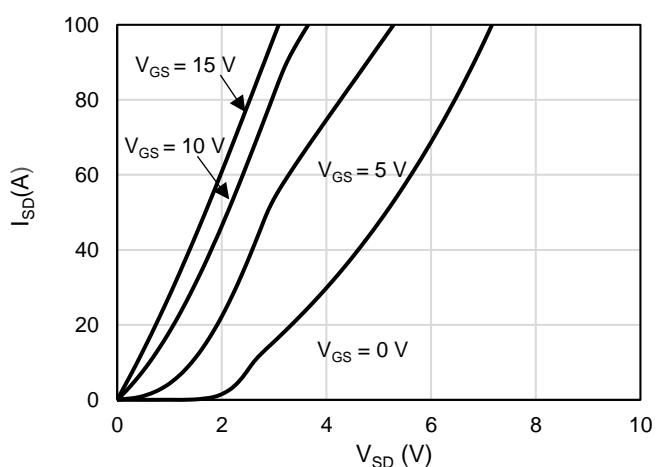
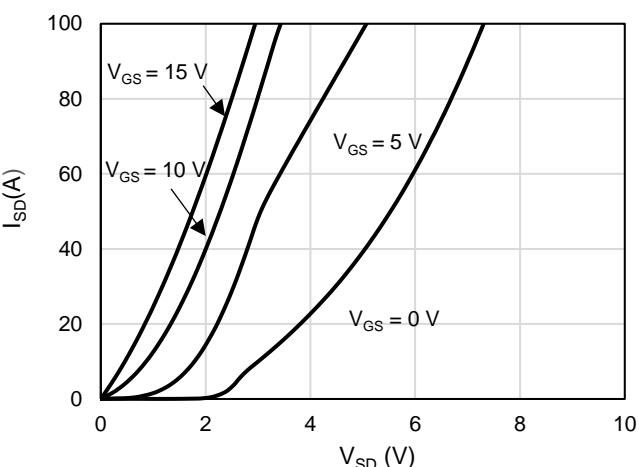


Figure 12: Gate-Charge Characteristics



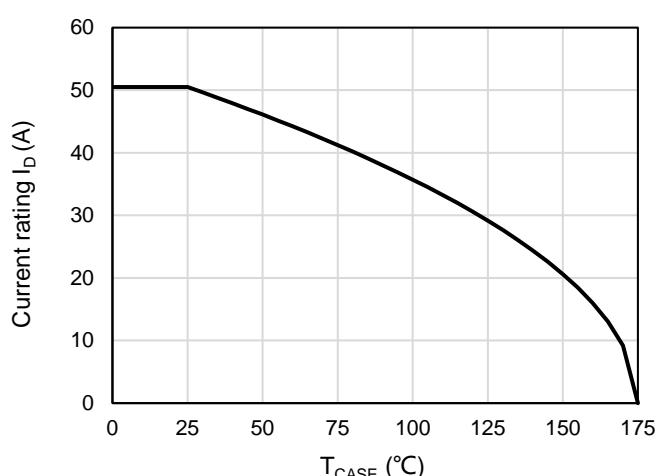


Figure 19: Current De-rating

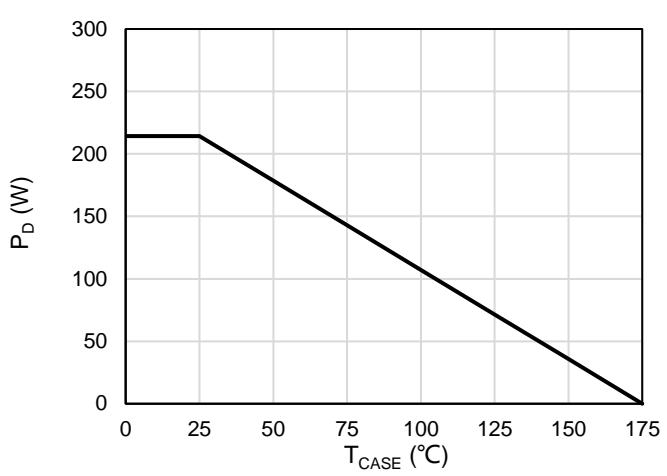


Figure 20: Maximum Power Dissipation Derating vs CaseTemperature

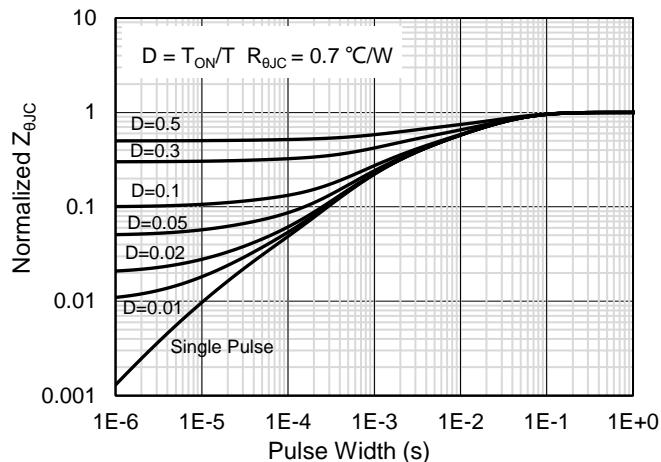


Figure 21: Normalized Maximum Transient Thermal Impedance

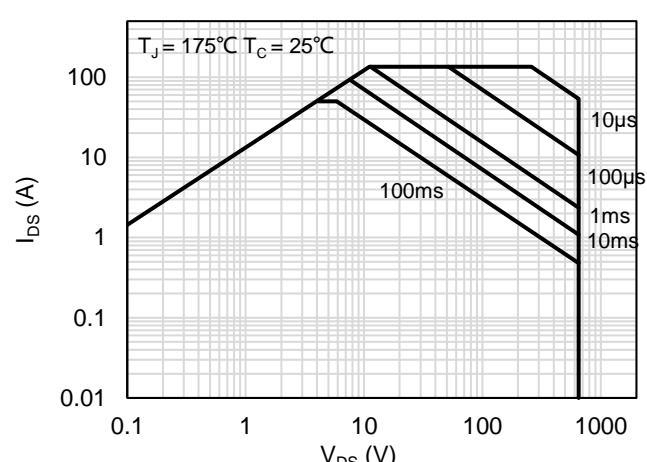


Figure 22: Maximum Forward Biased Safe Operating Area

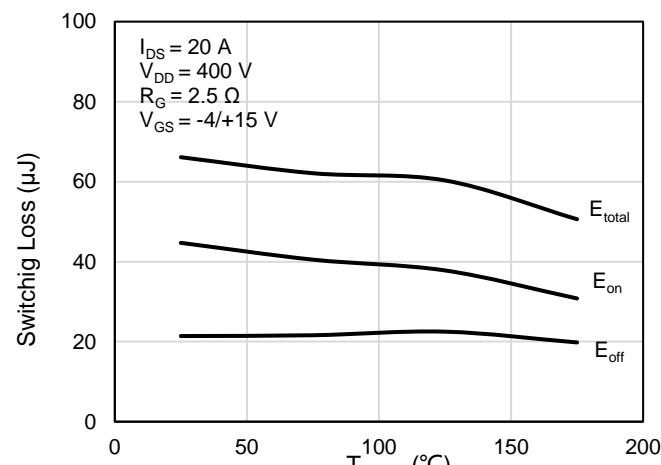
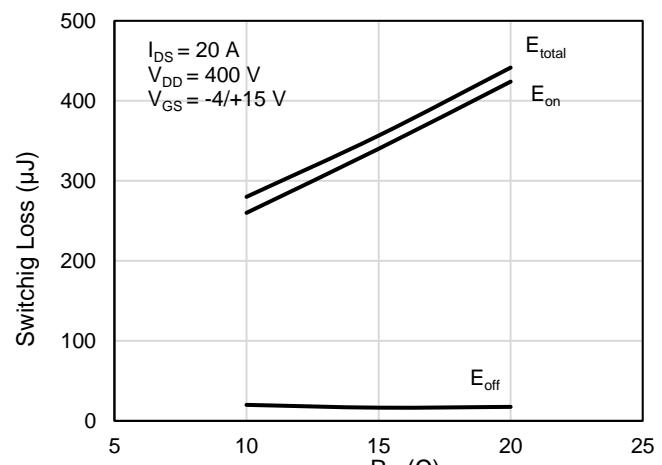
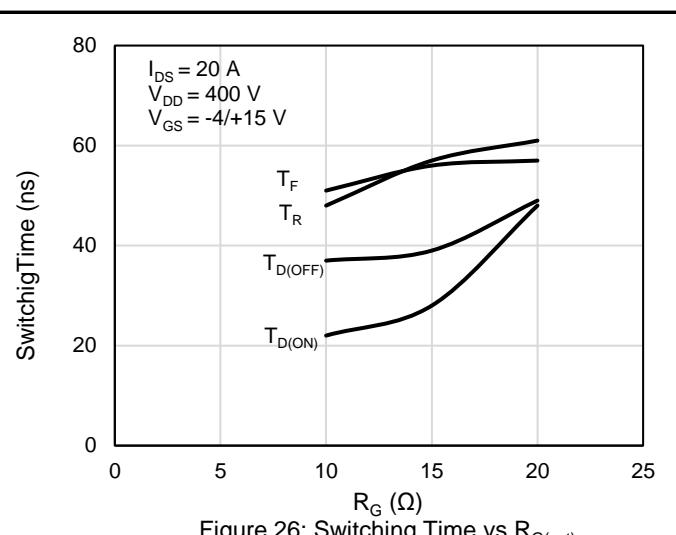
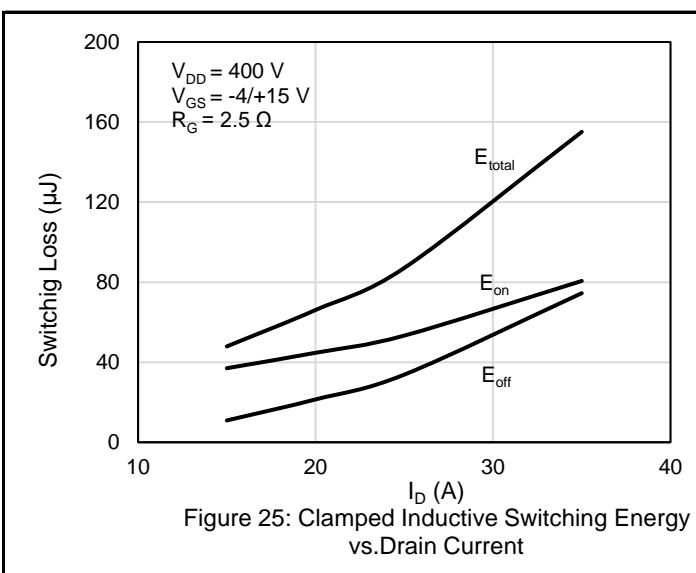


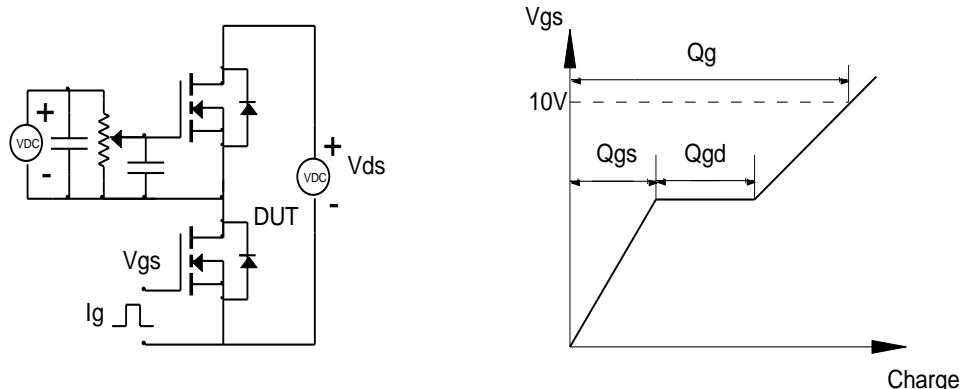
Figure 23: Clamped Inductive Switching Energy vs.Temperature

Figure 24: Clamped Inductive Switching Energy vs.R<sub>G</sub>(ext)

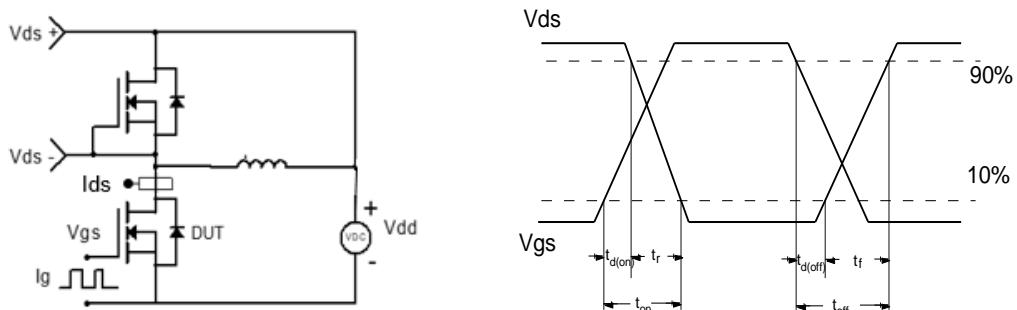


## Test Circuit and Waveform

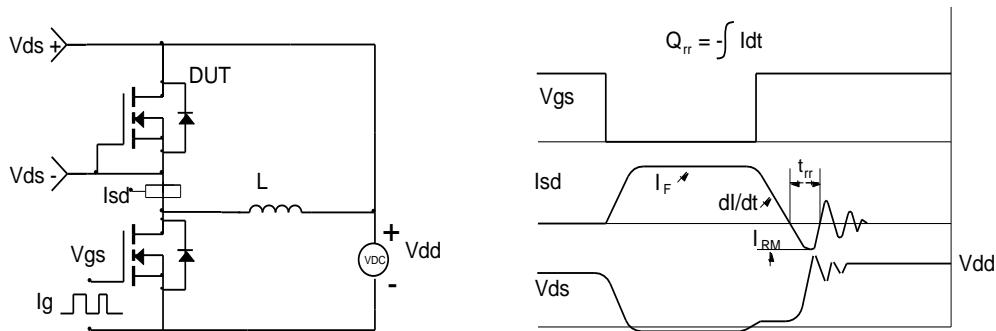
Gate Charge Test Circuit & Waveform



Clamped Inductive Switching Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



## Revision History

Revision	Release Date	Remark
Rev.1.0	2024	Initial Release

## Disclaimer

The information given in this document describes the independent performance of the product, but similar performance is not guaranteed under other working conditions, and cannot be guaranteed when installed with other products or equipment. To achieve the required performance of the product in actual scenarios, the customer should conduct a complete application test to assess the functionality of the product.

Alkaidsemi assumes no responsibility for equipment failures result from using products at values that exceed the ratings, operating conditions, or other parameters listed in the product specifications.

The product described in this specification is not applicable for aerospace or other applications which requires high reliability. Customers using or selling these products for use in medical, life-saving, or life-sustaining applications do so at their own risk and agree to fully indemnify.

Due to product or technical improvements, the information described or contained herein may be changed without prior notice.